



FIG. 1

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CONVENTIONAL HOT PLUG CAPABLE PCI EXPRESS PORT	mPHY POWER GATEABLE HOT PLUG CAPABLE PCI EXPRESS PORT
SHUT DOWN SQUELCH CIRCUIT FOR EVERY 12ms WHEN DEVICE IS UNPLUG	POWER GATE mPHY WHEN DEVICE IS UNPLUG AND CLKREQ# ON IS DE-ASSERTED

FIG. 2